

VISA-054

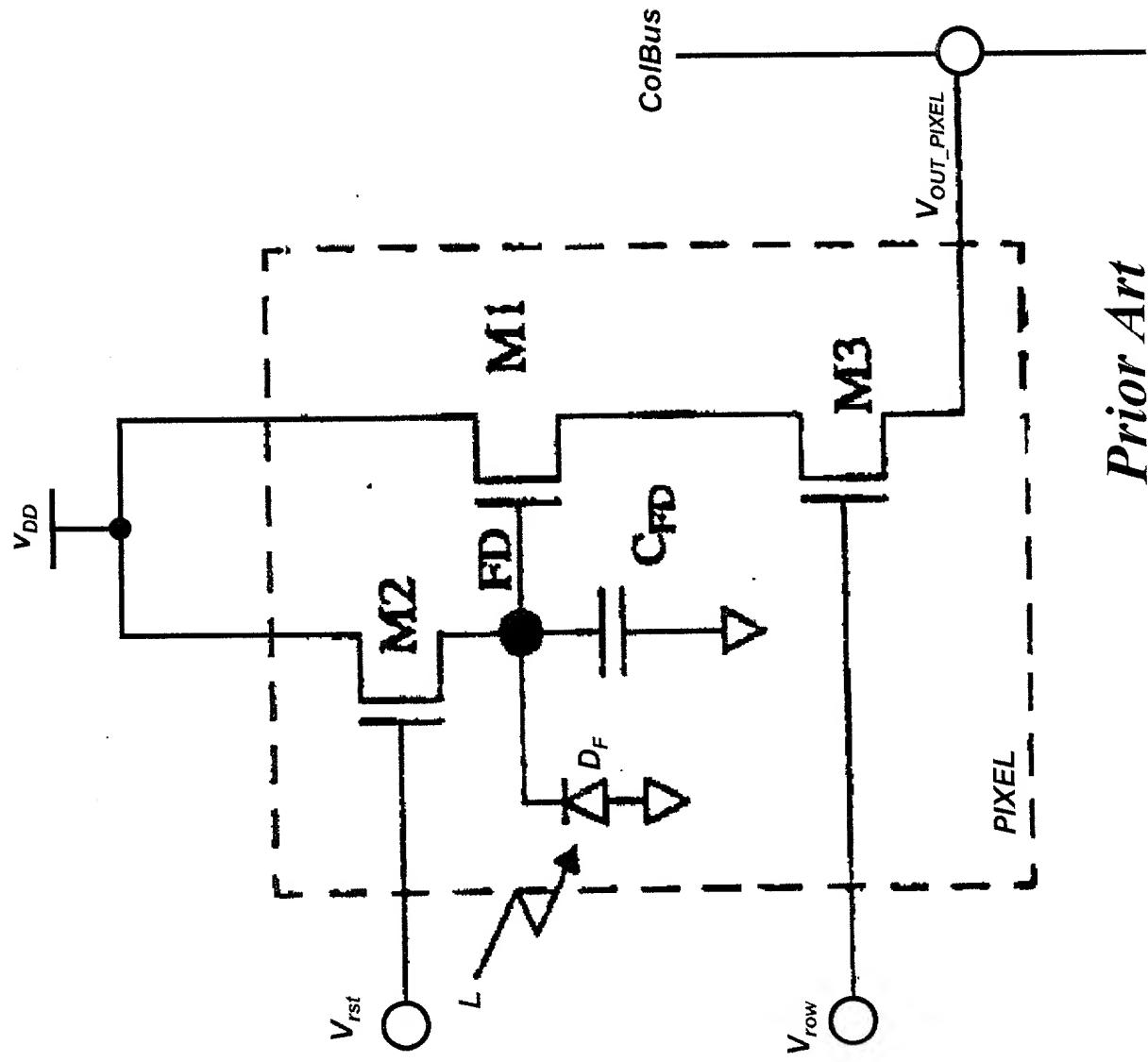


Fig. 1

Prior Art

VISA-054

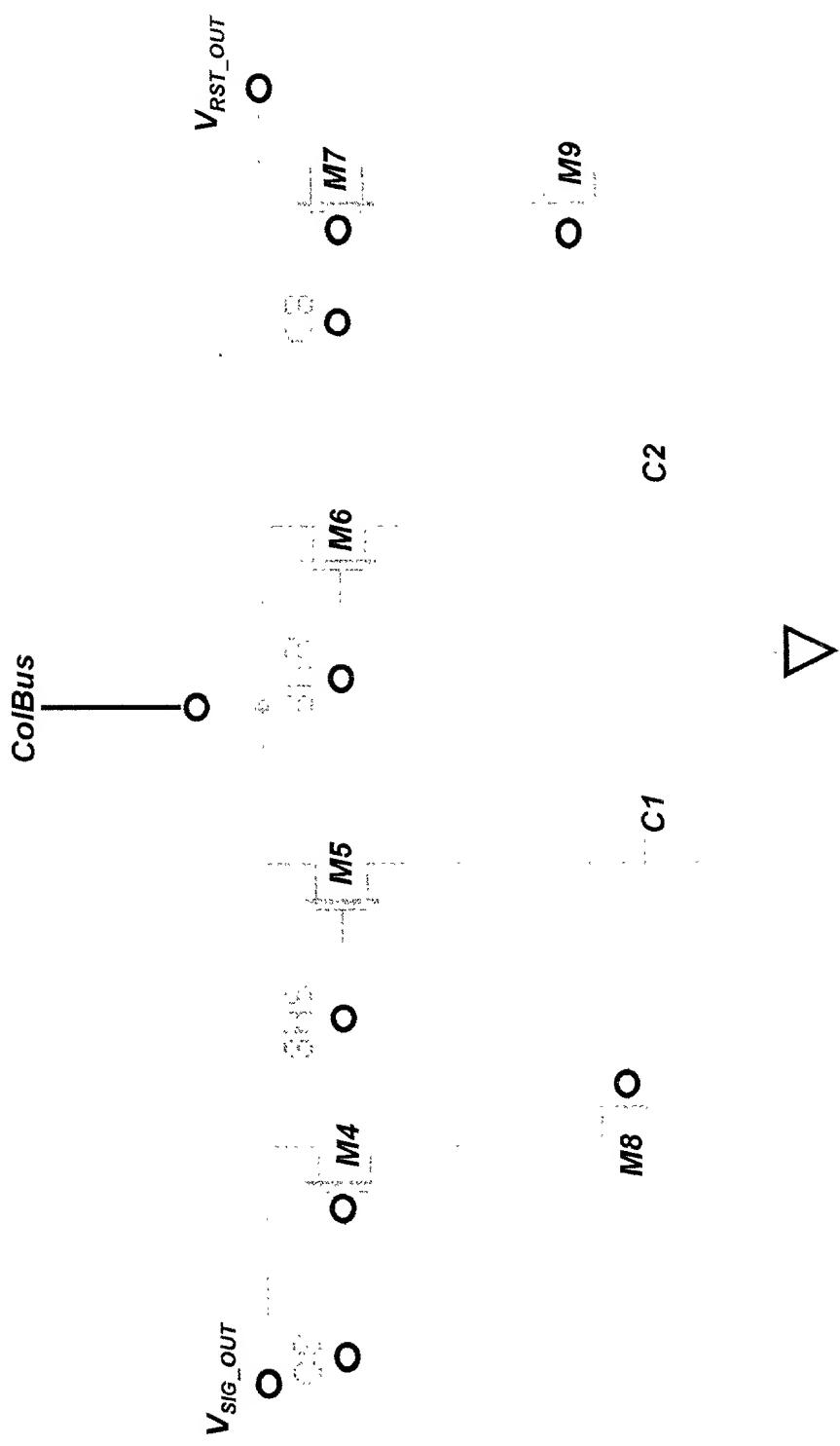


Fig. 2

Prior Art

VISA-054

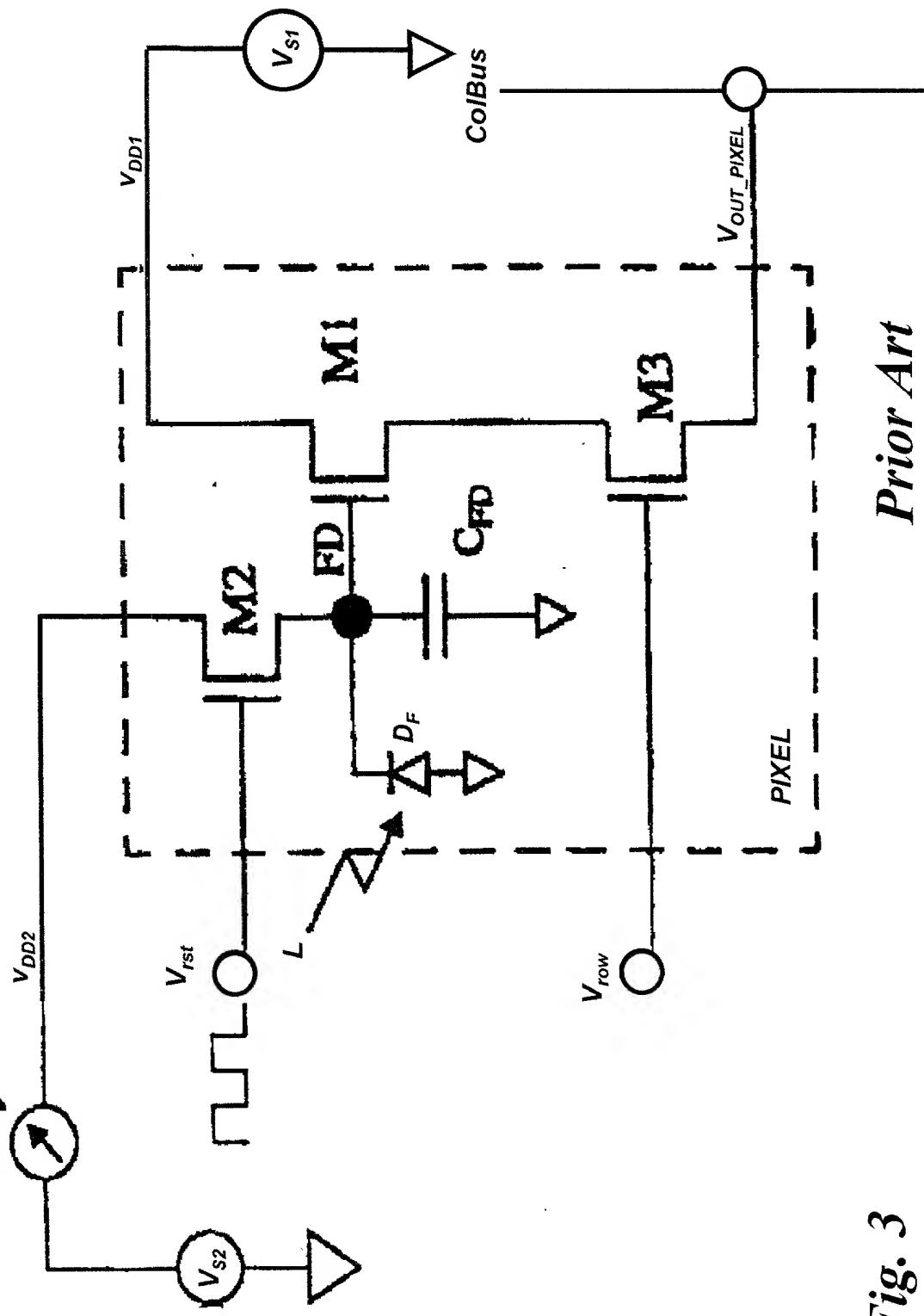


Fig. 3

Prior Art

VIS4-054

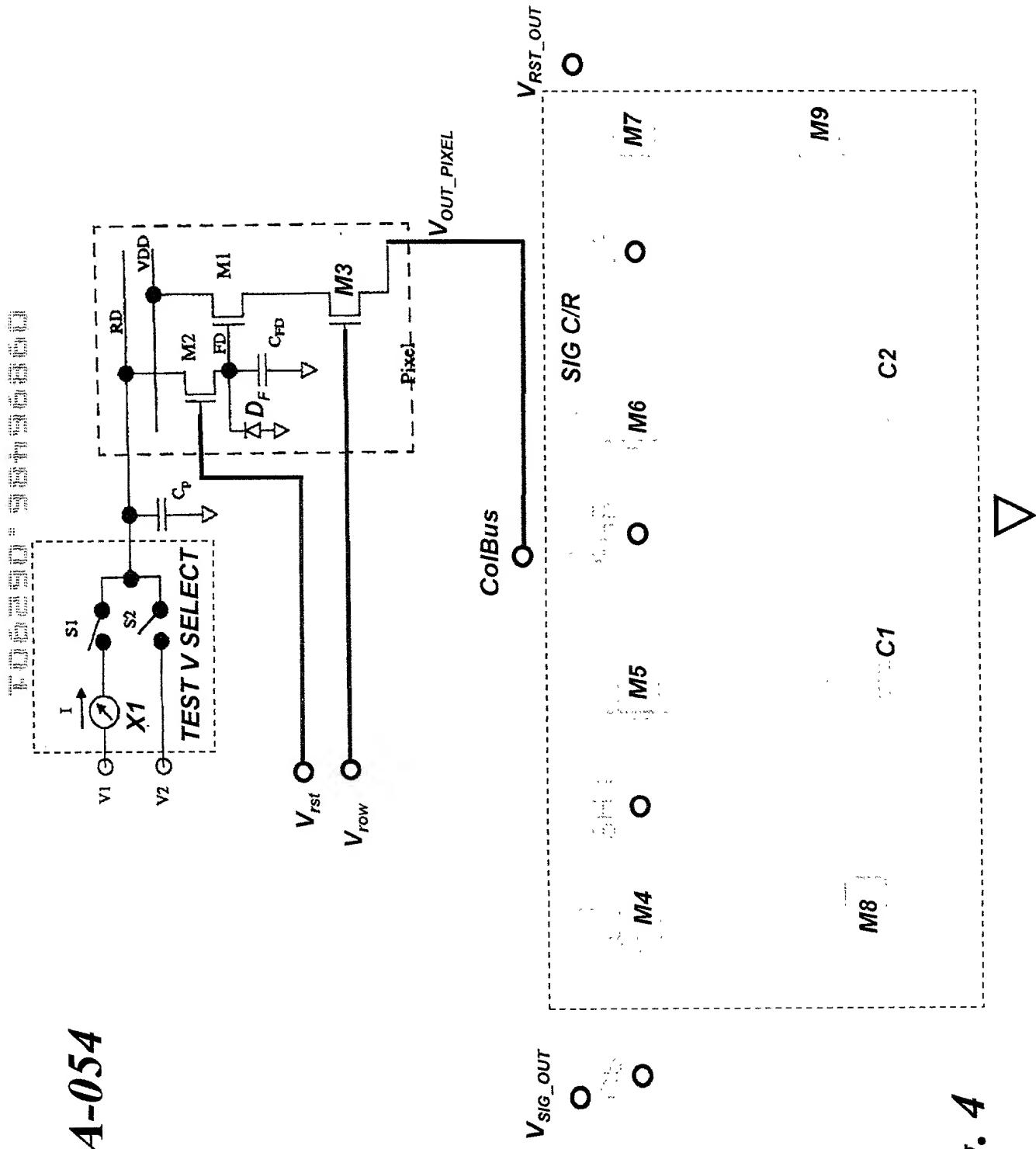


Fig. 4

VISA-054

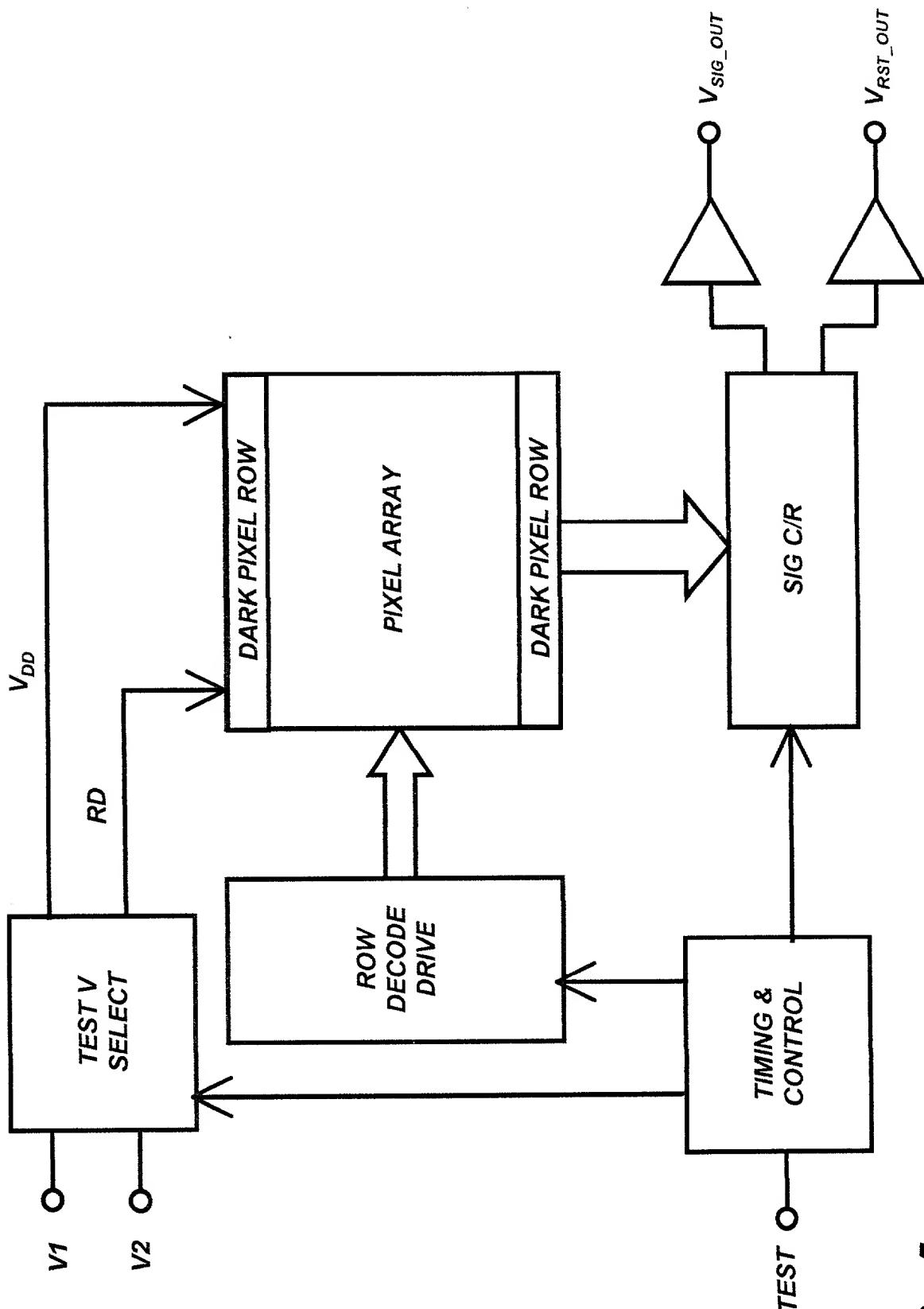


Fig 5

VISAs-054

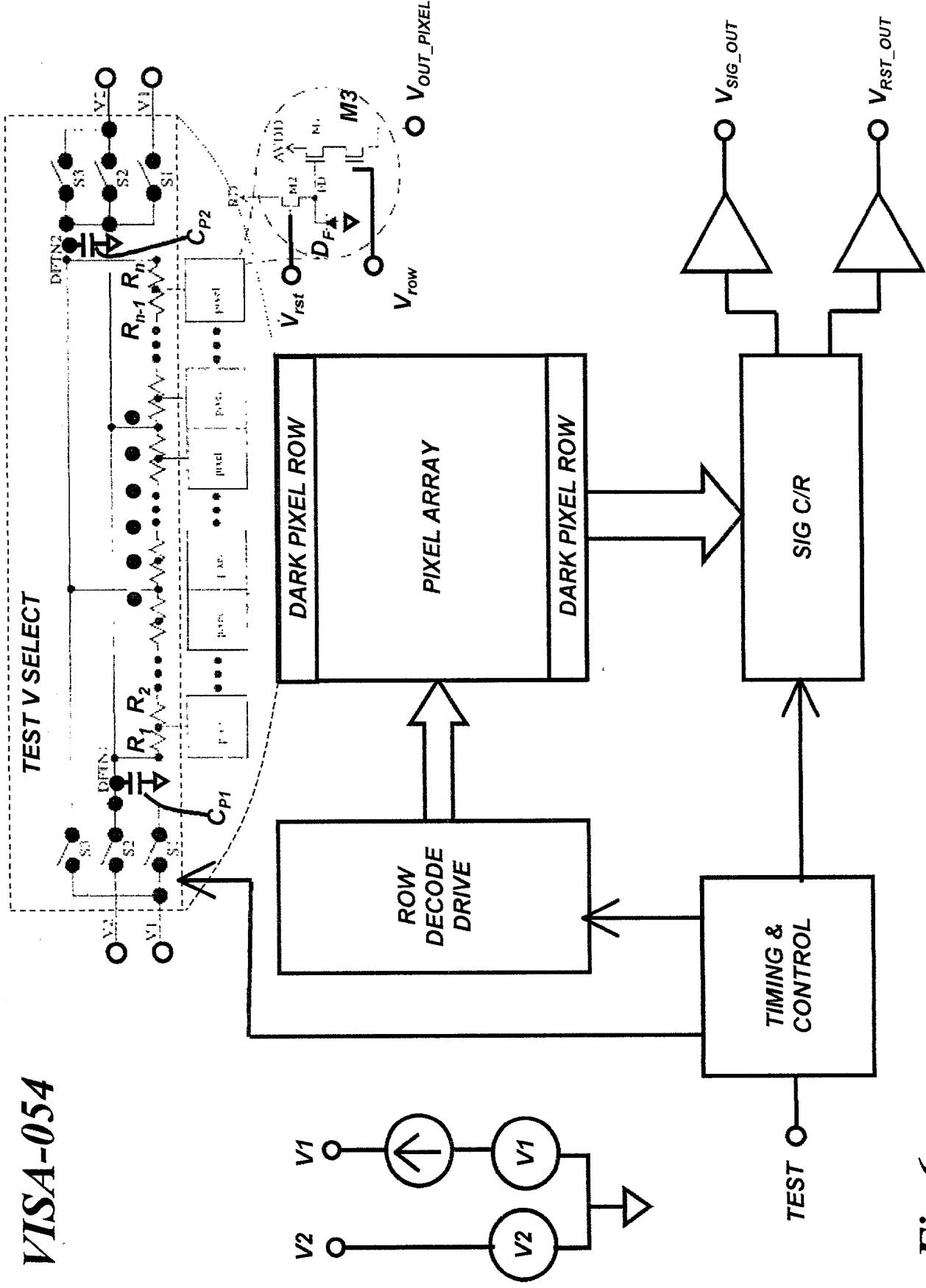


Fig 6

VISA-054

SHR V_{rst} SHS V_{row} S1 S2 S3 V_{DD} V_{out_pixel}

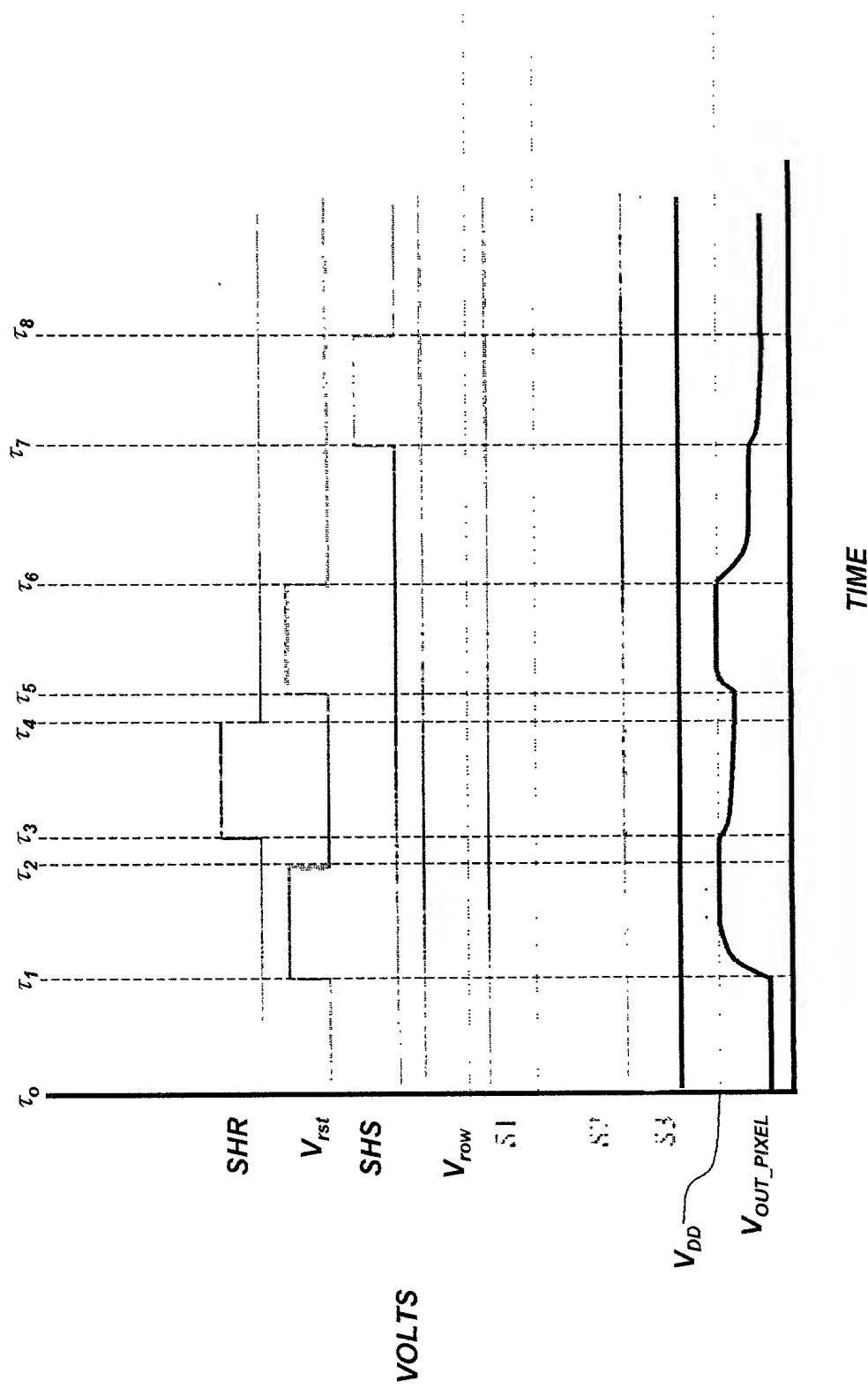


Fig 7a

VISA-054

Figure 7b: Timing diagram for the VISA-054 chip.

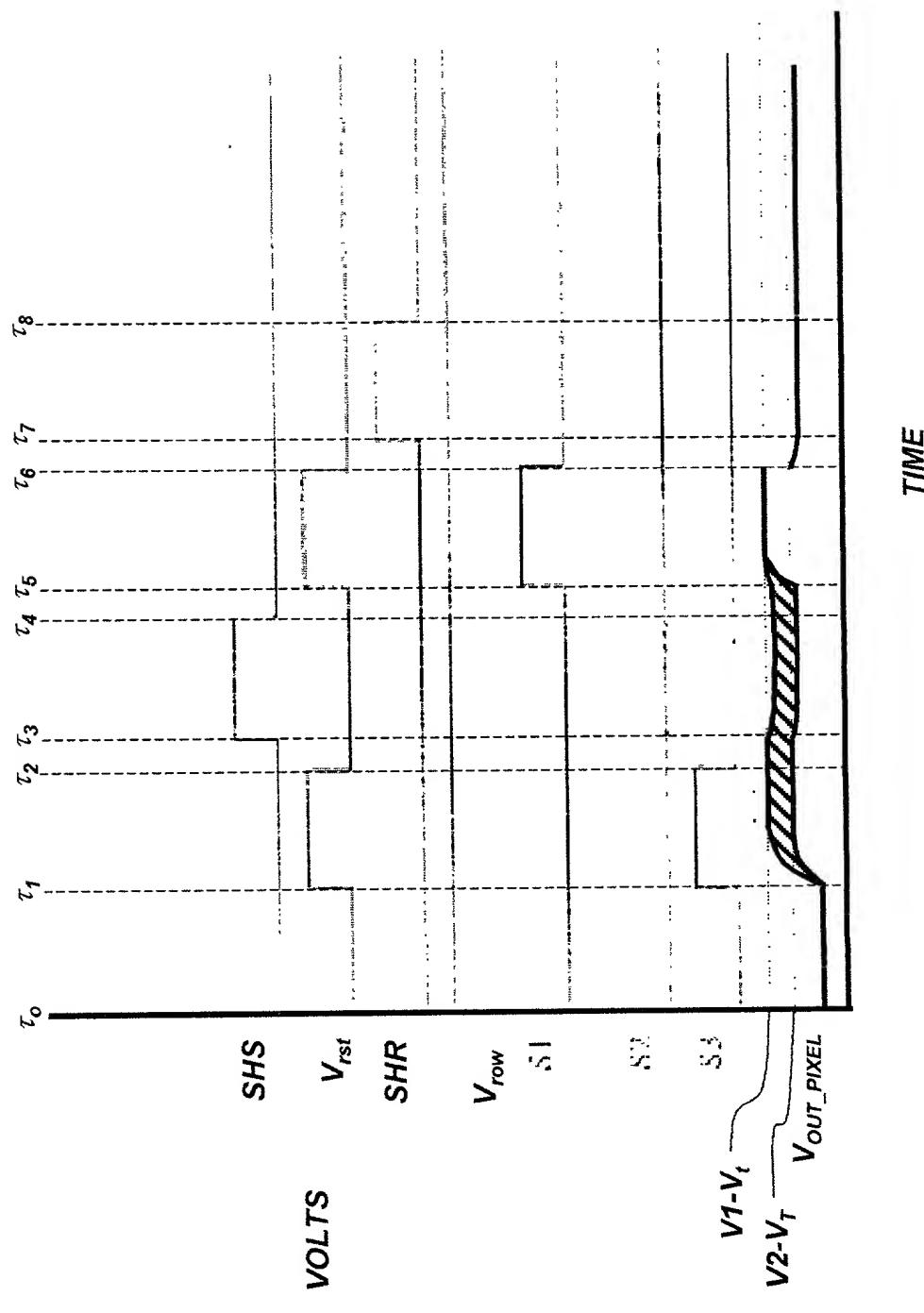


Fig 7b

VISA-054

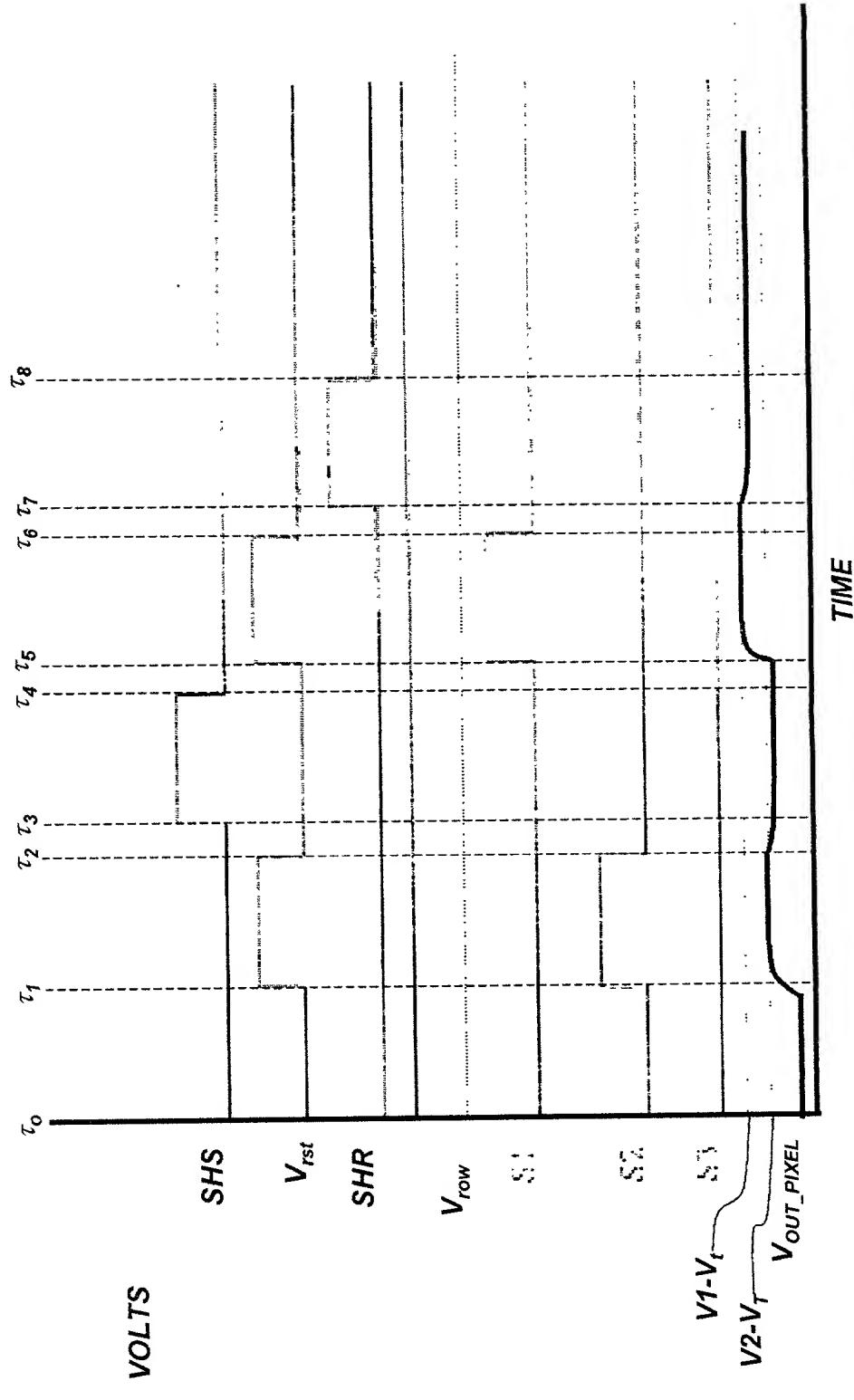


Fig 7c

VISA-054

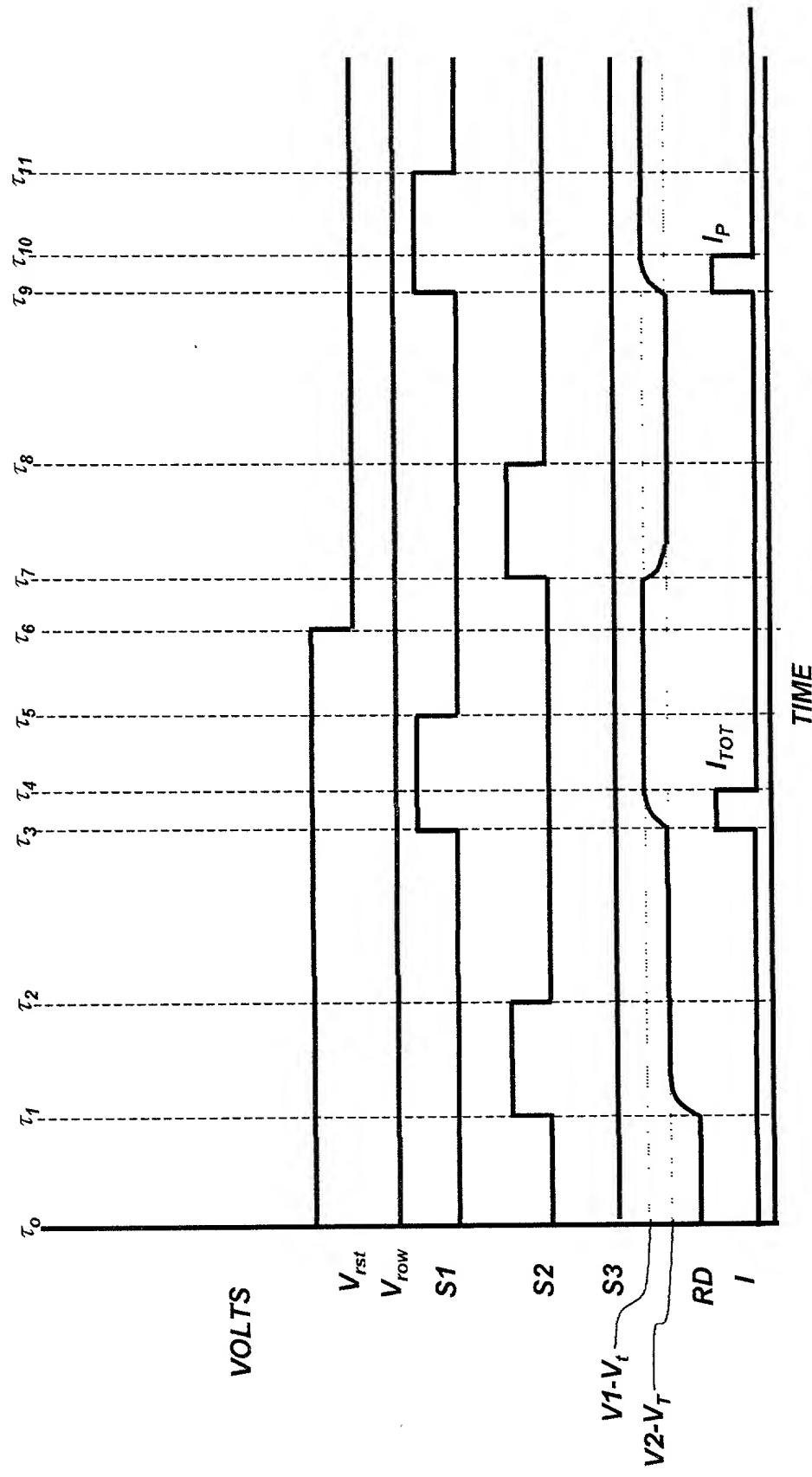


Fig 7d

VISA-054

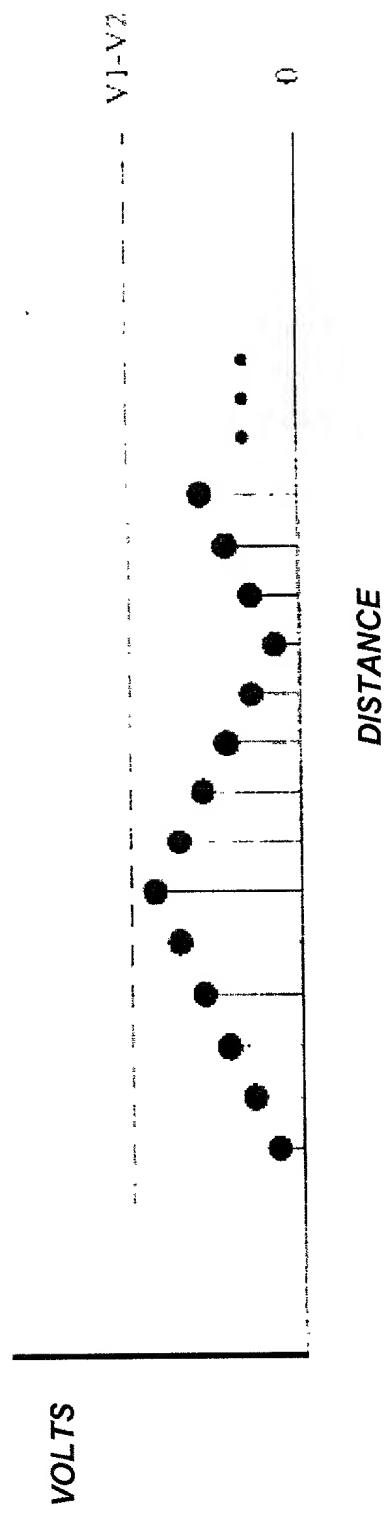


Fig 8

VISA-054

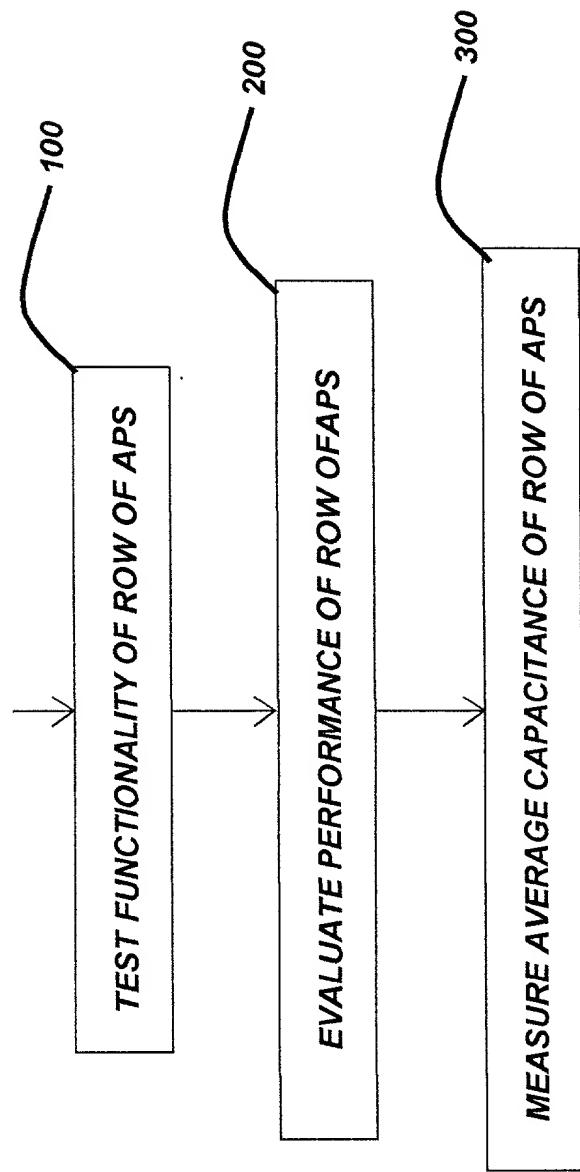


Fig 9a

VISA-054

100



Fig 9b

VISA-054

200

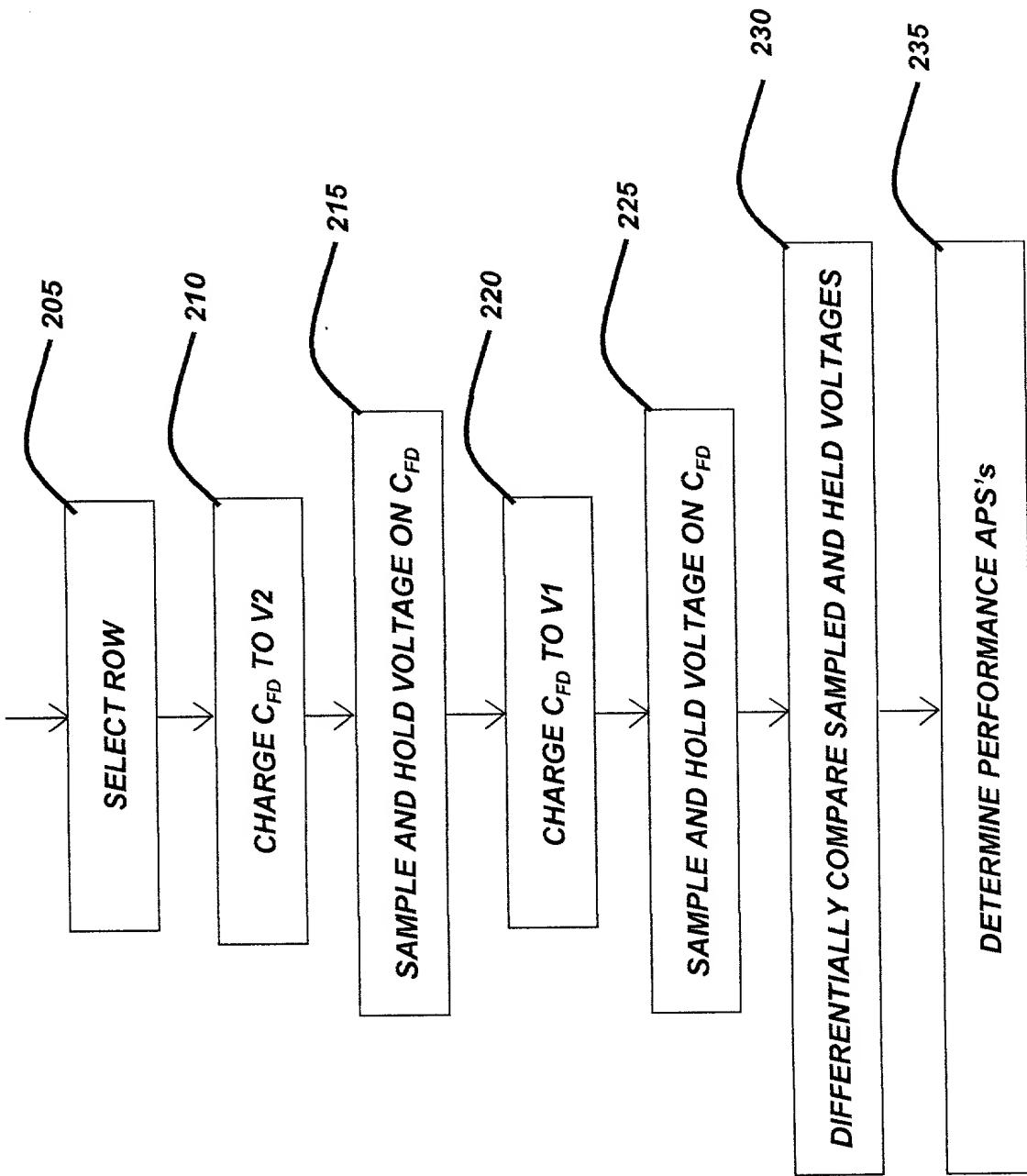
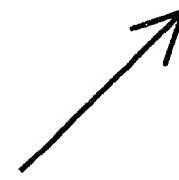


Fig 9c

VISA-054

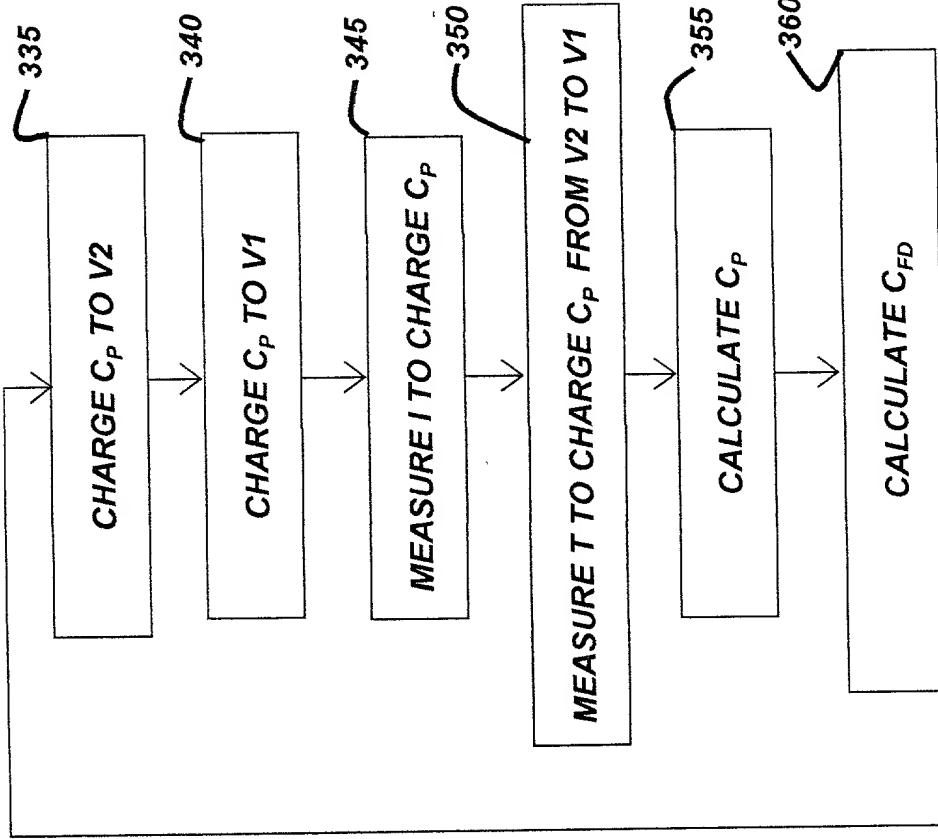
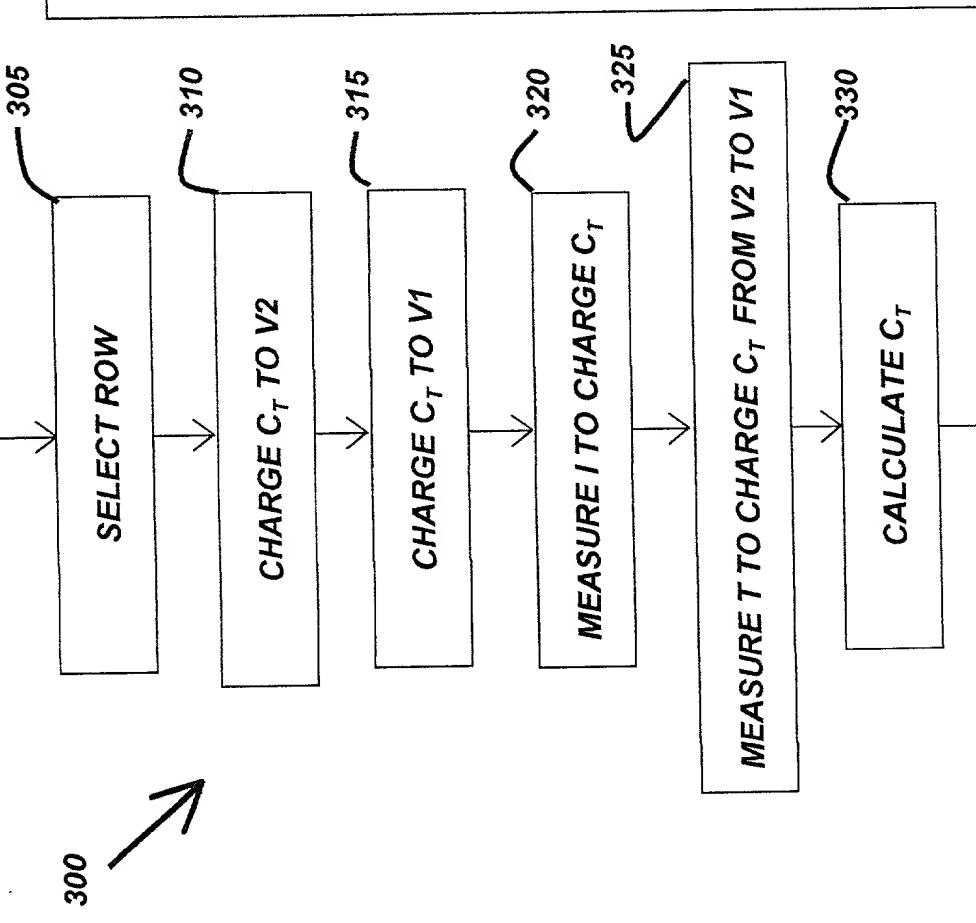


Fig 9d